**LESSON PLAN**

**Subject Code & Name: Microprocessor & Micro controller**

**Branch: E.C.E-B Class / Semester: III/IV-SEM 1I Academic Year:2017-18**

**Faculty: D.V.L.N.Sastry**

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| **Period** | **Date (Tentative)** | **Topic** | **Unit No.** | **Teaching Methodology** | **Remarks** | **Corrective action upon review** |
|  |  | **Micro Processor 8086** | **I** |  |  |  |
| 1 | 21.11.17 | Introduction |  | CR |  |  |
| 2 | 21.11.17 | Architecture |  | **PPT** |  |  |
| 3 | 22.11.17 | Register organization |  | CR |  |  |
| 4 | 24.11.17 | Memory segmentation & organization |  | CR |  |  |
| 5 | 28.11.17 | PIN diagram ,Signal description |  | PPT |  |  |
| 6 | 28.11.17 | Addressing Modes |  | PPT |  |  |
| 7 | 29.11.17 | Assembler Directives |  | CR |  |  |
| 8 | 1.12.17 | Procedures |  | CR |  |  |
| 9 | 5.12.17 | Macros |  | **CR** |  |  |
| 10 | 5.12.17 | Read Timing Diagram |  | CR |  |  |
| 11 | 6.12.17 | Write Timing Diagram |  | PPT |  |  |
| 12 | 8.12.17 | Maximum mode diagrams |  |  |  |  |
|  |  | **Assembly language programming of 8086** | **II** |  |  |  |
| 13 | 12.12.17 | Introduction |  | CR |  |  |
| 14 | 12.12.17 | Instruction set |  | PPT |  |  |
| 15 | 13.12.17 | Instruction set |  | PPT |  |  |
| 16 | 15.12.17 | Assembly language programmes |  | CR |  |  |
| 17 | 19.12.17 | Stack,interrupts |  | CR |  |  |
| 18 | 19.12.17 | ISR |  | CR |  |  |
| 19 | 20.12.17 | IVT |  | CR |  |  |
|  |  | **Advanced Micro Processors** | **III** |  |  |  |
| 20 | 2.1.18 | Introduciton |  |  |  |  |
| 21 | 2.1.18 | Architecture of 80386 |  | **PPT** |  |  |
| 22 | 3.1.18 | Register organization |  | PPT |  |  |
| 23 | 5.1.18 | Signal description |  | CR |  |  |
| 24 | 9.1.18 | Data types |  | CR |  |  |
| 25 | 9.1.18 | Physical address calculation |  | PPT |  |  |
| 26 | 10.1.18 | Real mode operation, Protected virtual mode operation |  | CR |  |  |
| 27 | 19.1.18 | Segmentation |  | CR |  |  |
| 28 | 23.1.18 | Paging unit |  | CR |  |  |
| 29 | 23.1.18 | Introduciton to 80486 |  | CR |  |  |
|  |  | **Interfacing with 8086** | **IV** |  |  |  |
| 30 | 30.1.18 | Programmable interrupt controller 8259A |  | PPT |  |  |
| 31 | 30.1.18 | Control words of 8259A |  | PPT |  |  |
| 32 | 2.2.18 | 8255 PPI |  | **PPT** |  |  |
| 33 | 6.2.18 | Modes of operation |  | CR |  |  |
| 34 | 6.2.18 | 8257 DMA controller |  | CR |  |  |
| 35 | 21.2.18 | Control words of 8257 |  | PPT |  |  |
| 36 | 23.2.18 | 8279 key board controller |  | PPT |  |  |
| 37 | 27.2.18 | Control words of 8279 |  | PPT |  |  |
| 38 | 27.2.18 | USART 8251 |  | PPT |  |  |
| 39 | 28.2.18 | Interfacing programs |  | PPT |  |  |
| 40 | 6.3.18 | DAC interfacing |  |  |  |  |
| 41 | 6.3.18 | ADC interfacing |  |  |  |  |
|  |  | **Micro controller 8051** |  |  |  |  |
| 42 | 7.3.18 | Introduction |  |  |  |  |
| 43 | 9.3.18 | Architecture |  | PPT |  |  |
| 44 | 13.3.18 | Signal description |  | PPT |  |  |
| 45 | 13.3.18 | Register set |  | PPT |  |  |
| 46 | 14.3.18 | Memory organization |  | CR |  |  |
| 47 | 16.3.18 | Parallel I/O ports |  | CR |  |  |
| 48 | 20.3.18 | Interrupts, Addressing modes |  | CR |  |  |
| 49 | 20.3.18 | Introduction to PIC micro controller |  | CR |  |  |
| 50 | 21.3.18 | 8051 programming |  | **CR** |  |  |

**CR: CLASS ROOM PPT: POWER POINT PRESENTATION LCD**